



**ANNEX TO THE EUROPEAN SEARCH REPORT  
ON EUROPEAN PATENT APPLICATION NO.**

EP 03 01 4110

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on  
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01-10-2003

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The present search report has been drawn up for all claims			
Place of search MUNICH		Date of completion of the search 1 October 2003	Examiner von Walter, S-U
<div>CATEGORY OF CITED DOCUMENTS</div> <div><div>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</div><div>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons ----- &amp; : member of the same patent family, corresponding document</div></div>			

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EPO FORM 1503 03.82 (P04C01)



DOCUMENTS CONSIDERED TO BE RELEVANT			
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The present search report has been drawn up for all claims			
Place of search MUNICH		Date of completion of the search 1 October 2003	Examiner von Walter, S-U
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## ABSTRACT / ZUSAMMENFASSUNG / ABREGE

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A high-frequency switch (10) comprises: a substrate (11); a main line electrode (12) provided between two terminals; a stub line electrode (15) with one end thereof connected to the side edge of the main line electrode and the other end thereof grounded; and a ground electrode (16) provided adjacent to the stub line electrode in the width direction thereof; wherein the substrate has a semiconductor activation layer (19) which extends to below the stub line electrode (15) and the ground electrode (16) between at least one side edge of the stubline electrode and the ground electrode; and wherein a gate electrode (20) which extends in the longitudinal direction of the stub line electrode is provided on the semiconductor activation layer between the stub line electrode and the ground electrode, thereby forming an FET structure, thus providing a high-frequency switch and electronic device therewith, capable of using high frequencies, having reduced insertion loss, and high signal cutoff capabilities.



P/1071-1595

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

New York, New York

Hiroyuki NAKANO

Date: October 27, 2003

Serial No.: 10/601,799

Group Art Unit: ---

Filed: June 23, 2003

Examiner: ---

For: HIGH-FREQUENCY SWITCH, AND ELECTRONIC DEVICE USING THE SAME

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

SUBMISSION

Sir:

Submitted herewith is a copy of a search report issued by a patent searching authority other than the PTO and the cited art together with a form listing the same for the convenience of the Examiner.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents P.O. Box 1450, Alexandria, VA 22313-1450, on October 27, 2003

James A. Finder

Name of applicant, assignee or  
Registered Representative

Signature

October 27, 2003

Date of Signature

Respectfully submitted,

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